

## **Delhi Technological University**

(Formerly Delhi College of Engineering) Consolidated Result Notification

No : 1566

## Program : Master of Technology

The following Candidates who appeared in Master of Technology End Semester examinations held in MAY, 2023 have completed the requirement and hence declared to have pass the final examinations. These Candidates have obtained 58 or more credits.

Branch : Orghan Processing & Digital Design - Department of Electronics & Communication Engineering				
Name	Roll No.	Name in Hindi	тс	CGPA
CGPA - Cumulative Grade Point Average		TC - Total Credits	58	
MEHRAB HOSAIN	2K21/SPD/01	मेहराब हुसैन	58	7.79
AKSHANSH SRIVASTAVA	2K21/SPD/02	आकाश श्रीवास्तव	58	7.33
DHRUV SHARMA	2K21/SPD/03	ध्रुव शर्मा	58	8.90
JAIDEEP KALA	2K21/SPD/04	जयदीप कला	58	9.02
PAULAMI PURKAYASTHA	2K21/SPD/09	पौलमी पुरकायस्थ	58	8.91
SONALIKA BHANDARI	2K21/SPD/13	सोनालिका भंडारी	58	9.48
TANISHQA TYAGI	2K21/SPD/16	तनिष्का त्यागी	58	8.67
SWASTIK MOHANTY	2K21/SPD/19	स्वस्तिक मोहंती	58	7.67
	Name   CGPA - Cumulative Grade Point Average   MEHRAB HOSAIN   AKSHANSH SRIVASTAVA   DHRUV SHARMA   JAIDEEP KALA   PAULAMI PURKAYASTHA   SONALIKA BHANDARI   TANISHQA TYAGI	NameRoll No.CGPA - Cumulative Grade Point AverageMEHRAB HOSAIN2K21/SPD/01AKSHANSH SRIVASTAVA2K21/SPD/02DHRUV SHARMA2K21/SPD/03JAIDEEP KALA2K21/SPD/04PAULAMI PURKAYASTHA2K21/SPD/09SONALIKA BHANDARI2K21/SPD/13TANISHQA TYAGI2K21/SPD/16	NameRoll No.Name in HindiCGPA - Cumulative Grade Point AverageTC - Total CreditsMEHRAB HOSAIN2K21/SPD/01मेहराब हुसैनAKSHANSH SRIVASTAVA2K21/SPD/02आकाश श्रीवास्तवDHRUV SHARMA2K21/SPD/03ध्रुव शर्माJAIDEEP KALA2K21/SPD/04जयदीप कलाPAULAMI PURKAYASTHA2K21/SPD/09पौलमी पुरकायस्थSONALIKA BHANDARI2K21/SPD/13सोनालिका भंडारीTANISHQA TYAGI2K21/SPD/16तनिष्का त्यागी	NameRoll No.Name in HindiTCCGPA - Cumulative Grade Point AverageTC - Total Credits58MEHRAB HOSAIN2K21/SPD/01मेहराब हुसैन58AKSHANSH SRIVASTAVA2K21/SPD/02आकाश श्रीवास्तव58DHRUV SHARMA2K21/SPD/03ध्रुव शर्मा58JAIDEEP KALA2K21/SPD/04जयदीप कला58PAULAMI PURKAYASTHA2K21/SPD/09पौलमी पुरकायस्थ58SONALIKA BHANDARI2K21/SPD/13सोनालिका भंडारी58TANISHQA TYAGI2K21/SPD/16तनिष्का त्यागी58

Branch : Signal Processing & Digital Design - Department of Electronics & Communication Engineering

Any discrepancy in the result in r/o name/roll no/registration should be brought to the notice of COE/office Acad (UG/PG) /OIC B.tech(Eve.) within 15 days of declaration of result , in the prescribed proforma.

Controller of Examination

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