Delhi Technological University
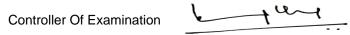
(Formerly Delhi College of Engineering)

Regular Result Notification

THE RESULT OF THE CANDIDATE WHO APPEARED IN THE FOLLOWING EXAMINATION HELD IN NOVEMBER , 2021 IS DECLARED AS UNDER : -

Notification No.   1375

Program : Master of Technology

Sem : I

Branch :   VLSI Design & Embedded System

TC : Total Credits   DT : Detained   AB : Absent   RL : Result Later   RW : Result Withdrawn

VLS501 : ANALOG IC DESIGN   VLS503 : DIGITAL CMOS IC DESIGN   VLS5401 : Digital Design With HDL (VERILOG)   VLS5307 : Organic and Flexible Electronics

VLS5201 : Seminar

Sr.No	Name	Roll No.	VLS501	VLS503	VLS5401	VLS5307	VLS5201	TC	SGPA	Papers Failed
		Credits	4	4	4	3	2			
1	ABHAY Anokha BANSAL	2K21/VLS/01	O	O	A+	A	A+	17	9.29	
2	ABHISHEK KUMAR	2K21/VLS/02	A+	A	A+	A	A+	17	8.59	
3	AKANSHA LAL	2K21/VLS/03	A+	A+	A	A	A+	17	8.59	
4	AMIT SINGH	2K21/VLS/04	A+	A+	A	A+	A	17	8.65	
5	DEBERJEET USHAM	2K21/VLS/05	A+	A+	A+	A	A+	17	8.82	
6	DIVYANSH SINGH	2K21/VLS/06	A+	A+	A	A	A	17	8.47	
7	GAURAV KASANA	2K21/VLS/07	A+	A+	O	A+	A+	17	9.24	
8	GAURAV SEHRAWAT	2K21/VLS/08	A+	O	A	A	A+	17	8.82	
9	HIMANSHU TIWARI	2K21/VLS/09	F	F	P	F	F	4	0.94	VLS501VLS503, VLS5307, VLS5201,
10	JUNED ALTAF MULANI	2K21/VLS/10	A+	A+	A+	A+	A+	17	9.00	
11	MANISHA	2K21/VLS/11	A+	A+	A	A	A	17	8.50	
12	MANISHA JAIN	2K21/VLS/12	A+	A+	O	A+	O	17	9.35	
13	MAYANK MANGLA	2K21/VLS/13	O	O	A+	A+	A+	17	9.47	

Any discrepancy in the result in r/o name/roll no/registration/marks/grades/course code/title should be brought to the notice of Controller of Examination/OIC B.tech(Eve.) within 15 days of declaration of result , in the prescribed proforma.

Date :   03/02/2022

Digitally Signed by Madhukar Cherukuri , OIC (Results)
valid only if downloaded from http://exam.dtu.ac.in

Controller Of Examination

THE RESULT OF THE CANDIDATE WHO APPEARED IN THE FOLLOWING EXAMINATION HELD IN NOVEMBER , 2021 IS DECLARED AS UNDER : -

Notification No. 1375

Program : Master of Technology

Sem : I

Branch : VLSI Design & Embedded System

TC : Total Credits   DT : Detained   AB : Absent   RL : Result Later   RW : Result Withdrawn

VLS501 : ANALOG IC DESIGN   VLS503 : DIGITAL CMOS IC DESIGN   VLS5401 : Digital Design With HDL (VERILOG)   VLS5307 : Organic and Flexible Electronics

VLS5201 : Seminar

Sr.No	Name	Roll No.	VLS501	VLS503	VLS5401	VLS5307	VLS5201		TC	SGPA	Papers Failed
		Credits	4	4	4	3	2				
14	MAYANK SHEKHAR	2K21/VLS/14	A+	A+	A+	A+	A		17	8.88	
15	RAM DUTT	2K21/VLS/15	A+	A+	A	A+	A		17	8.65	
16	RAVI RATAN JHA	2K21/VLS/16	A+	A+	A+	A	A		17	8.71	
17	SHIVAM KUMAR	2K21/VLS/17	A+	A+	O	A+	O		17	9.35	
18	SHWETA	2K21/VLS/18	A+	O	A+	A+	A+		17	9.24	
19	SUMER SAINI	2K21/VLS/19	A+	A	A	A+	A		17	8.41	
20	SURAJ SINGH	2K21/VLS/20	A+	A+	A	A	A		17	8.47	
21	PUNEET YADAV	2K21/VLS/21	A+	A+	A+	A+	A+		17	9.00	
22	VANSHIKA KUMAWAT	2K21/VLS/22	A+	A+	A+	A+	A+		17	9.00	
23	STUTI SINGH	2K21/VLS/23	A+	A+	A+	A+	A+		17	9.00	
24	SAKSHAM SINGH	2K21/VLS/24	A	B+	A	A	A		17	7.76	
25	MAHIMA SINGH YADAV	2K21/VLS/25	A+	A+	A	A	A+		17	8.59	

Any discrepancy in the result in r/o name/roll no/registration/marks/grades/course code/title should be brought to the notice of Controller of Examination/OIC B.tech(Eve.) within 15 days of declaration of result , in the prescribed proforma.

Date : 03/02/2022

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Controller Of Examination