Date: 29/06/2021

## **Delhi Technological University**

Visit - http://exam.dtu.ac.in

(Formerly Delhi College of Engineering) **Regular Result Notification** 

THE RESULT OF THE CANDIDATE WHO APPEARED IN THE FOLLOWING EXAMINATION HELD IN MAY, 2021 IS DECLARED AS UNDER: -

**Program: Master of Technology** 

Branch: VLSI Design & Embedded System

TC: Total Credits DT: Detained RL: Result Later RW: Result Withdrawn AB: Absent

VLS5406: CAD FOR VLSI SYSTEMS

VLS5208 : Layout Design and Skills with Analog Perspective VLS5302 : MINOR PROJECT

VLS504: LOW POWER VLSI DESIGN

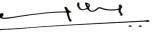
Sr.No	Name	Roll No.	VLS5208	VLS5302	VLS5406	VLS502	VLS504	TC	SGPA	Papers Failed
		Credits	2	3	4	4	4			
1	ABHINAV ANAND	2K20/VLS/01	0	A+	A+	0	0	17	9.59	
2	ANUKARNA SINHA	2K20/VLS/02	A+	0	0	A+	0	17	9.65	
3	BHUVAN JOSHI	2K20/VLS/03	B+	F	A+	B+	A	14	6.47	VLS5302,
4	DIVYA	2K20/VLS/04	A+	A+	0	A+	A+	17	9.24	
5	HARSH	2K20/VLS/05	A+	Α	0	0	A+	17	9.29	
6	KULDEEP SINGH BISHT	2K20/VLS/07	Α	A+	A+	A+	A+	17	8.88	
7	KUMAR SURYANSHU	2K20/VLS/08	A+	Α	0	Α	A+	17	8.82	
8	KUNDAN KUMAR DAS	2K20/VLS/09	Α	Α	A+	Α	A+	17	8.47	
9	MANISHA MISHRA	2K20/VLS/10	A+	Α	0	A+	A+	17	9.06	
10	MANSHI SINGH	2K20/VLS/11	Α	Α	0	B+	A+	17	8.47	
11	MOHD JEESHAN	2K20/VLS/12	Α	В	A+	Α	A+	17	8.12	
12	POORVA AGGARWAL	2K20/VLS/13	Α	Α	0	A+	A+	17	8.94	
13	PULKIT MITTAL	2K20/VLS/14	A+	Α	0	A+	A+	17	9.06	
14	RAKESH KUMAR RAY	2K20/VLS/15	Α	A+	A+	Α	0	17	8.88	
15	SACHIN KUMAR	2K20/VLS/16	A+	B+	A+	A+	A+	17	8.65	
16	SANDEEP KUMAR GUPTA	2K20/VLS/17	A+	Α	A+	A+	A+	17	8.82	
17	SHREYA GUPTA	2K20/VLS/18	0	Α	A+	Α	A	17	8.47	
18	SIDDHARTH SINGH	2K20/VLS/19	Α	A+	0	Α	0	17	9.12	
19	SRIHARSHA CHINTAMANENI	2K20/VLS/20	A+	A+	0	0	A+	17	9.47	
20	SUMIT GANGWAR	2K20/VLS/21	A+	A+	0	Α	A+	17	9.00	

Any discrepancy in the result in r/o name/roll no/registration/marks/grades/course code/title should be brought to the notice of Controller of Examination/OIC B.tech(Eve.) within 15 days of declaration of result , in the prescribed proforma.

Digitally Signed by Madhukar Cherukuri , OIC (Results)

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Controller Of Examination



Notification No.

VLS502: EMBEDDED SYSTEM DESIGN

1249

Sem: II

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VLS504: LOW POWER VLSI DESIGN

Sr.No	Name	Roll No.	VLS5208	VLS5302	VLS5406	VLS502	VLS504	TC	SGPA	Papers Failed
		Credits	2	3	4	4	4			
21	SURUCHI	2K20/VLS/22	A+	0	0	0	0	17	9.88	
22	VISHAL AGRAHARI	2K20/VLS/23	Α	A+	0	A+	0	17	9.35	
23	YATHAM NAGA REDDY	2K20/VLS/24	Α	0	0	A+	A+	17	9.29	
24	ZAHIRUL ISLAM	2K20/VLS/25	A+	Α	A+	B+	A+	17	8.35	
25	TARTEEL OSMAN	2K20/VLS/26	Α	Α	Α	B+	A	17	7.76	

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Sem: II