

## THE RESULT OF THE CANDIDATE WHO APPEARED IN THE FOLLOWING EXAMINATION HELD IN NOV 2023 IS DECLARED AS UNDER:-

## Master of Technology(VLSI Design & Embedded System), I-SEMESTER

Result Declaration Date: 2-02-2024 Notification No.: 1652

VLS5401 : Digital Design With HDL (VERILOG)

		Name of Student	VLS5401	ľ			Failed Courses	
Sr.N	o Roll No.		4.00	SGPA	тс	CGPA		
1	23/VLS/22	ANKIT TIWARI	F					

VLS501: ANALOG IC DESIGN VLS503: DIGITAL CMOS IC DESIGN VLS5201: Seminar VLS5307: Organic and Flexible Electronics VLS5401: Digital Design With HDL (VERILOG)

Sr.No	Roll No.	Name of Student	VLS501	VLS503	VLS5201	VLS5307	VLS5401	SGPA	TC	CGPA	Failed Courses
31.110	Holl No.	Name of Student	4.00	4.00	2.00	3.00	4.00	JULY	10		
2	23/VLS/25	YASHODA PARMAR	Α	0	0	A+	A+	9.12	17	9.118	

VLS501: ANALOG IC DESIGN VLS503: DIGITAL CMOS IC DESIGN VLS5201: Seminar VLS5307: Organic and Flexible Electronics VLS5401: Digital Design With HDL (VERILOG) VLS6207: Selected topics in VLSI VLS6305: Machine Learning

Sr.No	Roll No.	Name of Student	VLS501	VLS503	VLS5201	VLS5307	VLS5401	VLS6207	VLS6305	SGPA	тс	CGPA	Failed
			4.00	4.00	2.00	3.00	4.00	2.00	3.00				Courses
3	23/VLS/01	BRIJ NANDAN SINGH	0	A+	A+	A+	0	A+	B+	9.09	22	9.091	
4	23/VLS/03	SHASHI RANJAN UPADHYAY	B+	A+	Α	Α	A+	A+	Α	8.27	22	8.273	
5	23/VLS/04	PRAKHAR SAINI	B+	A+	A+	В	Α	Α	B+	7.68	22	7.682	
6	23/VLS/05	MOHAMMAD TALHA	В	B+	Α	Α	Α	Α	В	7.18	22	7.182	
7	23/VLS/06	PRAJVI UDAR	Α	Α	0	Α	A+	0	С	8.14	22	8.136	
8	23/VLS/07	MAYANK CHAUHAN	B+	Α	A+	B+	A+	B+	В	7.59	22	7.591	
9	23/VLS/08	AMARTYA ROY	Α	Α	A+	B+	B+	С	B+	7.36	22	7.364	
10	23/VLS/09	DEEPTI	B+	B+	A+	B+	Α	Α	В	7.32	22	7.318	

OIC (Results)

Controller of Examination



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11	23/VLS/10	AASTHA SINGH	Α	A+	A+	Α	Α	0	A+	8.59	22	8.591
12	23/VLS/11	GAJJAR JAY UMESH BHAI	A+	A+	0	B+	Α	B+	С	7.91	22	7.909
13	23/VLS/12	PREETI	В	Α	A+	B+	B+	Α	A+	7.55	22	7.545
14	23/VLS/13	ASMAR HAFEEZ	B+	Α	Α	В	B+	Α	Р	6.82	22	6.818
15	23/VLS/14	TUSHANT VERMA	B+	B+	A+	В	A+	Α	B+	7.5	22	7.5
16	23/VLS/15	AKHILESH VISHWAKARMA	B+	B+	A+	Α	Α	A+	B+	7.68	22	7.682
17	23/VLS/16	RAMLAKHAN	Α	Α	A+	Α	Α	A+	B+	8.05	22	8.045
18	23/VLS/17	DEVESH SAINI	B+	B+	Α	B+	Α	B+	B+	7.27	22	7.273
19	23/VLS/18	KUNCHA AKHILESH KUMAR	В	В	A+	В	B+	С	B+	6.5	22	6.5
20	23/VLS/19	SOUMYAJIT DAS	B+	Α	0	B+	A+	В	B+	7.73	22	7.727
21	23/VLS/20	UJJWAL BHARTI	В	Α	A+	B+	Α	B+	B+	7.36	22	7.364
22	23/VLS/21	TVISHA PUSHKAR	0	A+	A+	0	0	A+	В	9.09	22	9.091
23	23/VLS/23	MANISH SINGH	B+	A	A+	B+	B+	С	B+	7.18	22	7.182
24	23/VLS/24	ADITYA RAYER	В	Α	A+	В	A+	B+	В	7.27	22	7.273

VLS6207 : Selected topics in VLSI

Sr No	Roll No	Name of Student	VLS6207	SCDV	TC	CCDV	Failed Courses
31.140	HOII NO.	Name of Student	2.00	SGFA	10	CGFA	i alieu Courses
25	2K22/VLS/10	RAVINDER KAUR	Α	8	0	8	

TC - Total Secured Credits DT - Detained AB - Absent RL - Result Later RW - Result Withdrawn

OIC (Results)

**Controller of Examination**