

(Formerly Delhi College of Engineering)

Regular Result Notification

THE RESULT OF THE CANDIDATE WHO APPEARED IN THE FOLLOWING EXAMINATION HELD IN DEC 2019 IS DECLARED AS UNDER: -

Notification No.

1006

Sem: I

Program: MASTER OF TECHNOLOGY

Branch: VLSI Design & Embedded System

TC : Total Credits DT : Detained AB : Absent RL : Result Later RW : Result Withdrawn

VLS501 : ANALOG IC DESIGN VLS503 : DIGITAL CMOS IC DESIGN VLS5407 : IC Technology VLS5307 : Organic and Flexible Electronics

VLS5201 : Seminar

Date: 14/01/2020

Sr.No	Name	Roll No.	VLS501	VLS503	VLS5407	VLS5307	VLS5201	TC	SGPA	Papers Failed
		Credits	4	4	4	3	2			
1	AJIT BANDHU	2K19/VLS/01	В	B+	В	В	A	17	6.47	
2	ANSHIKA MALL	2K19/VLS/02	В	B+	B+	В	A+	17	6.82	
3	ANUPRIYA SRIVASTAVA	2K19/VLS/03	Α	Α	Α	B+	0	17	8.06	
4	ANURAG RAVISH	2K19/VLS/04	A+	A+	B+	Α	0	17	8.47	
5	DEEPAK CHAUHAN	2K19/VLS/05	В	В	С	С	A	17	5.82	
6	ISHITA SAGAR	2K19/VLS/06	В	Α	A+	B+	0	17	7.82	
7	JULI KUMARI ROY	2K19/VLS/07	Α	Α	B+	Α	A+	17	7.88	
8	MONIKA	2K19/VLS/08	B+	Α	Α	Α	A	17	7.76	
9	NIKKEE KUMARI	2K19/VLS/09	В	B+	Α	В	0	17	7.18	
10	PRATEEK RANJAN	2K19/VLS/10	Р	В	Α	B+	A	17	6.41	
11	PRIYA	2K19/VLS/11	Α	Α	Α	A+	A+	17	4.41	
12	RAJKUMAR GOLLU	2K19/VLS/12	В	Α	Α	A+	A+	17	7.82	
13	RASHMI BHARTI	2K19/VLS/13	Р	С	B+	С	A	17	5.59	
14	RAVULA SHIVARAMA KRISHNA	2K19/VLS/14	B+	Α	В	B+	A+	17	7.24	
15	SHASHANK PATHAK	2K19/VLS/16	С	Α	Α	A+	A+	17	7.59	
16	SHREYANSH	2K19/VLS/17	Α	Α	B+	Α	A+	17	7.88	
17	SHUBHAM GOYAL	2K19/VLS/18	Α	A+	Α	Α	A+	17	4.44	
18	SREELEKSHMI R NAIR	2K19/VLS/19	A+	0	A+	A+	0	17	9.35	
19	SUMIT	2K19/VLS/20	Α	Α	Α	A+	A	17	8.18	
20	UTKARSH GUPTA	2K19/VLS/21	С	B+	Α	B+	A	17	6.88	
21	VARUN DEV	2K19/VLS/22	В	B+	Р	B+	A	17	6.18	

Any discrepancy in the result in r/o name/roll no/registration/marks/grades/course code/title should be brought to the notice of Controller of Examination/OIC B.tech(Eve.) within 15 days of declaration of result , in the prescribed proforma.

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Controller Of Examination



Delhi Technological University

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THE RESULT OF THE CANDIDATE WHO APPEARED IN THE FOLLOWING EXAMINATION HELD IN DEC 2019 IS DECLARED AS UNDER: -

Notification No. 1

1006

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Branch: VLSI Design & Embedded System

TC : Total Credits DT : Detained AB : Absent RL : Result Later RW : Result Withdrawn

VLS501 : ANALOG IC DESIGN VLS503 : DIGITAL CMOS IC DESIGN VLS5407 : IC Technology VLS507 : Organic and Flexible Electronics

VLS5201 : Seminar

Date: 14/01/2020

Sr.No	Name	Roll No.	VLS501	VLS503	VLS5407	VLS5307	VLS5201	TC	SGPA	Papers Failed
		Credits	4	4	4	3	2			
22	VIJENDRA KUMAR SINGH	2K19/VLS/23	В	В	С	B+	A	17	6.18	
23	DOMINIC ABUGA ONGERI	2K19/VLS/24	С	С	С	В	A	17	5.53	

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