Delhi Technological University

(Formerly Delhi College of Engineering)

Regular Result Notification

THE RESULT OF THE CANDIDATE WHO APPEARED IN THE FOLLOWING EXAMINATION HELD IN DECEMBER 2019 IS DECLARED AS UNDER: -

Program: MASTER OF TECHNOLOGY (PART TIME)

Branch: VLSI Design & Embedded System

TC: Total Credits DT : Detained RL: Result Later RW: Result Withdrawn AB: Absent

VLS501: ANALOG IC DESIGN VLS503: DIGITAL CMOS IC DESIGN

Sr.	No Name	Roll No.	VLS501	VLS503	TC	SGPA	Papers Failed
		Credits	4	4			
1	SANJEEV MISHRA	2K19/VLS/501	F	F	0	0.00	VLS501, VLS503

Any discrepancy in the result in r/o name/roll no/registration/marks/grades/course code/title should be brought to the notice of Controller of Examination/OIC B.tech(Eve.) within 15 days of declaration of result, in the prescribed proforma.

Controller Of Examination

Visit - http://exam.dtu.ac.in

Notification No.

1017

Sem: I