Date: 09/09/2020

Delhi Technological University

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(Formerly Delhi College of Engineering)

Regular Result Notification

THE RESULT OF THE CANDIDATE WHO APPEARED IN THE FOLLOWING EXAMINATION HELD IN JUNE, 2020 IS DECLARED AS UNDER: -

Program: Master of Technology

Branch: VLSI Design & Embedded System

TC : Total Credits DT : Detained AB : Absent RL : Result Later RW : Result Withdrawn

VLS502 : EMBEDDED SYSTEM DESIGN VLS504 : LOW POWER VLSI DESIGN VLS5408 : CMOS RF CIRCUIT DESIGN VLS5302 : MINOR PROJECT

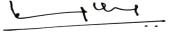
VLS5204: REAL TIME OPERATING SYSTEM

| Sr.No | Name | Roll No. | VLS502 | VLS504 | VLS5408 | VLS5302 | VLS5204 | TC | SGPA | Papers Failed |
|-------|-----------------------------|-------------|--------|--------|---------|---------|---------|----|-------|---------------|
| | | Credits | 4 | 4 | 4 | 3 | 2 | | | |
| 1 | AJIT BANDHU | 2K19/VLS/01 | Α | A+ | Α | Α | A | 17 | 8.24 | |
| 2 | ANSHIKA MALL | 2K19/VLS/02 | Α | A+ | Α | A+ | A | 17 | 8.41 | |
| 3 | ANUPRIYA SRIVASTAVA | 2K19/VLS/03 | A+ | A+ | Α | A+ | A+ | 17 | 8.76 | |
| 4 | ANURAG RAVISH | 2K19/VLS/04 | Α | A+ | A+ | A+ | A+ | 17 | 8.76 | |
| 5 | DEEPAK CHAUHAN | 2K19/VLS/05 | B+ | Α | Α | Α | A | 17 | 7.76 | |
| 6 | ISHITA SAGAR | 2K19/VLS/06 | A+ | Α | 0 | A+ | 0 | 17 | 9.12 | |
| 7 | JULI KUMARI ROY | 2K19/VLS/07 | A+ | Α | Α | A+ | A+ | 17 | 8.53 | |
| 8 | MONIKA | 2K19/VLS/08 | A+ | Α | A+ | A+ | A+ | 17 | 8.76 | |
| 9 | NIKKEE KUMARI | 2K19/VLS/09 | B+ | Α | Α | A+ | B+ | 17 | 7.82 | |
| 10 | PRATEEK RANJAN | 2K19/VLS/10 | Α | Α | Α | Α | B+ | 17 | 7.88 | |
| 11 | PRIYA | 2K19/VLS/11 | A+ | A+ | A+ | A+ | 0 | 17 | 9.12 | |
| 12 | RAJKUMAR GOLLU | 2K19/VLS/12 | Α | A+ | A+ | A+ | A+ | 17 | 8.76 | |
| 13 | RASHMI BHARTI | 2K19/VLS/13 | Α | B+ | Α | Α | В | 17 | 7.53 | |
| 14 | RAVULA SHIVARAMA KRISHNA | 2K19/VLS/14 | Α | Α | Α | Α | A | 17 | 8.00 | |
| 15 | SHASHANK PATHAK | 2K19/VLS/16 | Α | 0 | Α | A+ | A+ | 17 | 8.76 | |
| 16 | SHREYANSH | 2K19/VLS/17 | A+ | A+ | Α | A+ | A | 17 | 8.65 | |
| 17 | SHUBHAM GOYAL | 2K19/VLS/18 | A+ | 0 | 0 | A+ | A+ | 17 | 9.47 | |
| 18 | SREELEKSHMI R NAIR | 2K19/VLS/19 | 0 | 0 | 0 | 0 | 0 | 17 | 10.00 | |
| 19 | SUMIT | 2K19/VLS/20 | A+ | 0 | Α | A+ | A+ | 17 | 9.00 | |
| 20 | UTKARSH GUPTA | 2K19/VLS/21 | A+ | Α | Α | A+ | A | 17 | 8.41 | |

Any discrepancy in the result in r/o name/roll no/registration/marks/grades/course code/title should be brought to the notice of Controller of Examination/OIC B.tech(Eve.) within 15 days of declaration of result , in the prescribed proforma.

Digitally Signed by Madhukar Cherukuri, OIC (Results) valid only if downloaded from http://exam.dtu.ac.in

Controller Of Examination



Notification No.

1111

Sem: II



Date: 09/09/2020

Delhi Technological University

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VLS502: EMBEDDED SYSTEM DESIGN VLS504: LOW POWER VLSI DESIGN VLS5408: CMOS RF CIRCUIT DESIGN VLS5302: MINOR PROJECT

VLS5204: REAL TIME OPERATING SYSTEM

| Sr.No | Name | Roll No. | VLS502 | VLS504 | VLS5408 | VLS5302 | VLS5204 | TC | SGPA | Papers Failed |
|-------|----------------------|-------------|--------|--------|---------|---------|---------|----|------|---------------|
| | | Credits | 4 | 4 | 4 | 3 | 2 | | | |
| 21 | VARUN DEV SINGH | 2K19/VLS/22 | B+ | В | Α | Α | B+ | 17 | 7.18 | |
| 22 | VIJENDRA KUMAR SINGH | 2K19/VLS/23 | B+ | B+ | Α | B+ | В | 17 | 7.12 | |
| 23 | ONGERI ABUGA DOMINIC | 2K19/VLS/24 | В | B+ | B+ | В | B+ | 17 | 6.59 | |

Any discrepancy in the result in r/o name/roll no/registration/marks/grades/course code/title should be brought to the notice of Controller of Examination/OIC B.tech(Eve.) within 15 days of declaration of result, in the prescribed proforma.