



**Delhi Technological University**  
(Formerly Delhi College of Engineering)  
**Result Notification**

Visit - <http://exam.dtu.ac.in>

No.DTU/Results/MTECH/DEC/2015/

THE RESULT OF THE CANDIDATES WHO APPEARED IN THE FOLLOWING EXAMINATIONS HELD IN DEC-2015 IS DECLARED AS UNDER : -

DEC/2015

Program : Master of Technology

Sem : III

**Department of Electronics & Communication Engineering - VLSI Design & Embedded System**

EC-7111:FAULT TOLERANT COMPUTING

EC-7113:SOFT COMPUTING

EC-713:SELF STUDY OPEN AREA SEMINAR - II

EC-714:MINOR PROJECT-II

EC-715:MAJOR PROJECT-I

TC: Total Credits

D: Detained

A: Absent

RL: Result Later

RW: Result Withdrawn

Sr.No.	Name	Roll No.	EC-7111	EC-7113	EC-713	EC-714	EC-715	TC	SGPA
		<b>Max. Marks / Credits</b>	150/3	150/3	100/2	300/6	300/6	20	Papers Failed
1	ASHISH KUMAR NAG	2K13/VLS/03	77	94	86	225	240	20	7.95

Any discrepancy in the result in r/o name/roll no/registration should be brought to the notice of A.R. (Acad.)/OIC B.Tech. (Eve.) within 15 days of declaration of result, in the prescribed proforma. ANY discrepancy in r/o MARKS, apply for RECHECKING.