

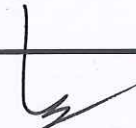
DELHI TECHNOLOGICAL UNIVERSITY

Bawana Road, Delhi - 110 042.


SUPPLEMENTARY EXAMINATION (FEBRUARY- 2019)

DATE SHEET: M.TECH (FT/PT)

Date & Day	First Semester (FT & PT) & Third Semester (PT) 10:00 AM to 01.00 PM			Third Semester (FT) & Fifth Semester (PT) 02.00 PM to 05.00 PM		
12.02.2019 (Tuesday)	STE	CE502	Advanced Theory of Structure	GTE	CE7221	Earth Pressure & Earth Retaining Structure
	CSE	CO502	Parallel Computer Architecture	SPD	EC7214	New Technologies in Image Processing
	MOC	EC501	Advanced Communication Systems	BIO	BT7011	Immunoinformatics
	VLS	EC511	Analog IC Design			
	SPD	EC521	Advanced DSP			
	ENE	AM511	Advanced Mathematics			
13.02.2019 (Wednesday)	HFE	CE532	Advanced Hydrology	VLS	EC7123	Advanced Topics in VLSI Design
	STE	CE501	Advanced Mathematics & Numerical Techniques	C&I	EE7122	Advanced Control System Design
	CSE	CO501	Advanced Database Management System			
	VLS	EC512	IC Technology			
	PSY	EE 502	Integrated Electronics & Applied Instrumentation.			
14.02.2019 (Thursday)	STE	CE503	Finite Element Method of Structural Analysis			
	HFE	CE533	Advanced Open Channel Hydraulics			
	VLS	EC513	VLSI Design			
	SPD	EC523	Statistical Maths & Wavelet Theory			
	C&I	EE513	Process Instrumentation & Control			
	PIE	ME503	Welding Processes & Metallurgy			
	THE	ME513	Optimization Techniques			



15.02.2019 (Friday)	STE	CE551	Theory of Plates & Elastic Stability
	CSE	CO551	Distributed System
	MOC	EC551	Advanced Electromagnetic Theory
	VLS	EC561	Digital System Design
	C & I	EE561	Modeling , Identification & Control
	THE	ME561	Advanced Heat & Mass Transfer
	CDN	ME581	Optimization Techniques in Design
16.02.2019 (Saturday)	MOC	EC 552	Numerical Techniques in Electromagnetics
	VLS	EC562	Digital Signal Processing
	SPD	EC572	Adaptive Signal Processing
	C&I	EE562	Discrete Data Systems & Digital Control
	ISY	IT552	Digital Image Processing & Analysis


05.2.19
 (KAMAL PATHAK)
 Controller of Examination